

## INTERNATIONAL SEARCH REPORT

Inten Application No  
PC/1, 2004/051372

A. CLASSIFICATION OF SUBJECT MATTER  
IPC 7 G06F15/80

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, INSPEC, COMPENDEX

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	<p>DAHLE D M ET AL: "Kestrel: design of an 8-bit SIMD parallel processor" ADVANCED RESEARCH IN VLSI, 1997. PROCEEDINGS., SEVENTEENTH CONFERENCE ON ANN ARBOR, MI, USA 15-16 SEPT. 1997, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 15 September 1997 (1997-09-15), pages 145-162, XP010256320 ISBN: 0-8186-7913-1 page 146, section 2 page 151, section 3.3, paragraph 3 page 153, paragraphs 2,3; figure 5</p> <p style="text-align: center;">----- -/-</p>	<p>1-10, 12-26, 28-32</p>

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

## \* Special categories of cited documents:

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
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\*&\* document member of the same patent family

Date of the actual completion of the international search

18 February 2005

Date of mailing of the international search report

11/03/2005

Name and mailing address of the ISA

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Intern

Application No

PCT, 102004/051372

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	KLEIHORST R P ET AL: "Xetal: a low-power high-performance smart camera processor" ISCAS 2001. PROCEEDINGS OF THE 2001 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS. SYDNEY, AUSTRALIA, MAY 6 - 9, 2001, IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS, NEW YORK, NY : IEEE, US, vol. VOL. 1 OF 5, 6 May 2001 (2001-05-06), pages 215-218, XP010542070 ISBN: 0-7803-6685-9 figure 1	1-10, 12-26, 28-32
X	OKAZAKI S ET AL: "Integrated memory array processor and real-time vision system for vehicle control" VEHICLE NAVIGATION AND INFORMATION SYSTEMS CONFERENCE, 1994. PROCEEDINGS., 1994 YOKOHAMA, JAPAN 31 AUG.-2 SEPT. 1994, NEW YORK, NY, USA, IEEE, 31 August 1994 (1994-08-31), pages 103-106, XP010136590 ISBN: 0-7803-2105-7 page 104, left-hand column, paragraph 3	1,2,11, 15-18, 27,31
X	EP 0 638 868 A (HUGHES AIRCRAFT COMPANY) 15 February 1995 (1995-02-15)  column 3, line 25 column 4, lines 9-14	1,2,11, 15-18, 27,31,32
X	BLEVINS D W ET AL: "BLITZEN: a highly integrated massively parallel machine" FRONTIERS OF MASSIVELY PARALLEL COMPUTATION, 1988. PROCEEDINGS., 2ND SYMPOSIUM ON THE FRONTIERS OF FAIRFAX, VA, USA 10-12 OCT. 1988, WASHINGTON, DC, USA, IEEE COMPUT. SOC. PR, US, 10 October 1988 (1988-10-10), pages 399-406, XP010033009 ISBN: 0-8186-5892-4 page 401, left-hand column page 401, right-hand column, last paragraph	1,2, 16-18,32
X	DE 101 59 283 A1 (SYSTEMONIC AG) 12 June 2003 (2003-06-12) columns 1-2	1,2, 16-18,32

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